

<b>Notice of References Cited</b>	Application/Control No. 09/939,716	Applicant(s)/Patent Under Reexamination YONENAGA ET AL.	
	Examiner Danny Wai Lun Leung	Art Unit 2633	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,543,952	08-1996	Yonenaga et al.	398/185
*	B	US-5,644,664	07-1997	Burns et al.	385/2
*	C	US-5,917,638	06-1999	Franck et al.	398/201
	D	US-			
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chapter 7, Introduction to CMOS design, by Eitienne Sicard, National Institute of Applied Sciences, Department of Ele. & Comp. Engineering, ( <a href="http://www.esng.dibe.unige.it/Projects/Netpro/PagineLocali/micro/doc/ch7i.PDF">http://www.esng.dibe.unige.it/Projects/Netpro/PagineLocali/micro/doc/ch7i.PDF</a> ), Copyrighted 1997, 2001.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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